

IN THE SPECIFICATION:

Page 1, line 11, insert:

This application is a filing under 35 USC 371 of
PCT/EP2004/008048 filed July 19, 2004.

Background of the Invention

Page 3, line 16, insert:

Summary of the Invention

delete lines 24-25.

Page 5, lines 31-34:

The object is solved by a circuit arrangement according
~~to the features of the characterizing part of claim 16,~~ in
which a summer is provided for the phase-locked addition of
the amplitude values stored in the storage module.

Page 8, line 16, insert:

Brief Description of the Drawings

Page 10, line 7, insert:

Description of the Preferred Embodiments